L Number	Hits	Search Text	DB	Time stamp
1	3668336	test access port controller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/06 16:25
2	124	TAPC	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/06 16:25
3	3668397	(test access port controller) or TAPC	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/06 16:26
4	2936125	test access port or TAP	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/06 16:29
5	1157213	multi-core IC processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/06 16:30
6	418172	(test access port or TAP) and (multi-core IC processor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/06 16:30
7	7183	((test access port controller) or TAPC) and ((test access port or TAP) and (multi-core IC processor))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/06 16:31
9	1657735	processor core$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/06 16:39
10	918851	non-processor core$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/06 16:31
11	918851	(processor core$1) and (non-processor core$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/06 16:31
12	1027	((processor core$1) and (non-processor core$1)) and (((test access port controller) or TAPC) and ((test access port or TAP) and (multi-core IC processor)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/06 16:34
13	1039	"IEEE 1149.1"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/06 16:34
14	1	(((processor core$1) and (non-processor core$1)) and (((test access port controller) or TAPC) and ((test access port or TAP) and (multi-core IC processor)))) and "IEEE 1149.1"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/06 16:34
15	6684	714/30 or 714/724 or 714/725 or 714/10 or 714/11	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/06 16:41

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16	3	(((processor core$1) and (non-processor core$1)) and (((test access port controller) or TAPC) and ((test access port or TAP) and (multi-core IC processor)))) and (714/30 or 714/724 or 714/725 or 714/10 or 714/11)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/06 16:46
17	17	(((test access port controller) or TAPC) and ((test access port or TAP) and (multi-core IC processor))) and (714/30 or 714/724 or 714/725 or 714/10 or 714/11)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/06 16:50
18	2351234	test controller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/06 16:50
19	5727	(((test access port controller) or TAPC) and ((test access port or TAP) and (multi-core IC processor))) and (test controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/06 16:50
20	16	((((test access port controller) or TAPC) and ((test access port or TAP) and (multi-core IC processor))) and (test controller)) and ((((test access port controller) or TAPC) and ((test access port or TAP) and (multi-core IC processor))) and (714/30 or 714/724 or 714/725 or 714/10 or 714/11))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/06 16:50